AMENDMENT

Please amend the application as follows:

In the Claims:

Please cancel without prejudice claims 3-7, 12-16, and 26-35 (claim 2 canceled previously). Please amend claims 1, 8, 11, and 17. The current version of the claims follows:

1. (Currently amended) A radio-frequency (RF) apparatus capable of transmitting radio-frequency signals, the radio-frequency apparatus comprising:

transmitter path circuitry, including:

a voltage-controlled oscillator circuitry configured to generate a first signal that has a first frequency; ~~and~~

a divider circuitry being responsive to the first signal, the divider circuitry configured to generate a second signal that has a second frequency,

wherein the second frequency equals the first frequency divided by a number;

a first switch coupled to the voltage-controlled oscillator circuitry, the first switch configured to generate a switched first signal by selectively supplying the first signal to a first output;

a second switch coupled to the divider circuitry, the second switch configured to generate a switched second signal by selectively supplying the second signal to a second output; and

a feedback circuitry coupled to the voltage-controlled oscillator circuitry, the feedback circuitry configured to adjust the first frequency in response to a feedback signal derived from the switched first and second signals, and wherein the feedback circuitry comprises a third switch coupled to the first and second switches, the third switch configured to selectively supply as the feedback signal one of the switched first and second signals.

2-7. (Canceled).

8. (Currently amended) The radio-frequency (RF) apparatus according to claim ~~7~~1, wherein the transmitter path circuitry is configured to use the switched first and second signals for multi-band radio-frequency transmission.

9. (Previously presented) The radio-frequency (RF) apparatus according to claim 8, further configured to transmit within the DCS 1800 standard.

10. (Previously presented) The radio-frequency (RF) apparatus according to claim 8, further configured to transmit within the GSM 900 standard.

11. (Currently amended) An integrated circuit, comprising:

a controlled oscillator configured to generate an oscillator output signal with an adjustable frequency;

a feedback circuit coupled to the controlled oscillator, the feedback circuit configured to receive the oscillator output signal, the feedback circuit further configured to adjust the frequency of the oscillator output signal;~~ and~~

a frequency converter coupled to the controlled oscillator, the frequency converter configured to generate a converter output signal at a frequency that is derived from the frequency of the oscillator output signal;

a first switch coupled to the controlled oscillator, the first switch configured to selectively provide the oscillator output signal as a first output signal;

a second switch coupled to the frequency converter, the second switch configured to selectively provide the converter output signal as a second output signal, wherein the frequency of the converter output signal equals a fraction of the frequency of the oscillator output signal; and

a feedback circuit comprising a third switch coupled to the first and second switches, the third switch configured to selectively supply as a feedback signal one of the first and second output signals.

12-16. (Canceled).

17. (Currently amended) The integrated circuit according to claim ~~16~~11, wherein the feedback circuit further comprises a first feedback circuit and a second feedback circuit.

18. (Previously presented) The integrated circuit according to claim 17, wherein the first feedback circuit is configured to coarsely adjust the frequency of the oscillator output signal.

19. (Previously presented) The integrated circuit according to claim 18, wherein the second feedback circuit is configured to fine tune the frequency of the oscillator output signal.

20. (Previously presented) The integrated circuit according to claim 19, wherein the second feedback circuit is further configured to fine tune the frequency of the oscillator output signal in response to the feedback signal.

21. (Previously presented) The integrated circuit according to claim 20, wherein the first feedback circuit is further configured to coarsely adjust the frequency of the oscillator output signal in response to a reference signal.

22. (Previously presented) The integrated circuit according to claim 21, wherein the second feedback circuit is further configured to fine tune the frequency of the oscillator output signal in response to a radio-frequency local oscillator signal and an intermediate-frequency signal.

23. (Previously presented) The integrated circuit according to claim 22, further comprising receiver circuitry configured to receive a radio-frequency input signal.

24. (Previously presented) The integrated circuit according to claim 23, wherein the receiver circuitry couples to signal processing circuitry within a second integrated circuit.

25. (Previously presented) The integrated circuit according to claim 24, wherein the receiver circuitry comprises low intermediate-frequency receiver circuitry.

26-35. (Canceled).